

IN THE CLAIMS:

Please cancel claims 1-19 and add new claims 20-25 as follows:

1 ~~Sub B1~~ 20. (New) In a video data processing apparatus including a controller coupled  
2 to a memory circuit that has a plurality of arrays, a method for storing video data in the  
3 memory circuit comprising the steps of:  
4 segmenting a plurality of pixels representing one horizontal line of an image  
5 display panel into a plurality of pixel groups; and  
6 storing data representing each of said plurality of pixel groups, respectively, in a  
7 row of a plurality of non-adjoining arrays in the memory circuit.

1 21. (New) The method of claim 20 further comprising the steps of:  
2 defining tiles of pixels by a two-dimensional grouping of the plurality of pixel  
3 groups from a plurality of horizontal lines of the display panel; and  
4 storing data representing each tile of pixels substantially entirely in non-  
5 adjoining arrays in the memory circuit.

1 ~~Sub B2~~ 22 (New) The method of claim 21 wherein the data representing each of said  
2 plurality of pixel groups is respectively stored in the same row of the plurality of non-adjoining  
3 arrays.

1 23. (New) The method of claim 22 wherein the data representing each tile of  
2 pixels is respectively stored in consecutive rows of the plurality of non-adjoining arrays.